

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Nadeem N. Eleyan, et al.

Title: TEST CIRCUIT FOR MEASURING SENSE AMPLIFIER AND MEMORY MISMATCHES

Application No.: 10/683,636

Filed: October 10, 2003

Examiner: Not Yet Assigned

Group Art Unit: 2818

Atty. Docket No.: 004-30059

March 3, 2004

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- ☒ Form(s) PTO-1449  
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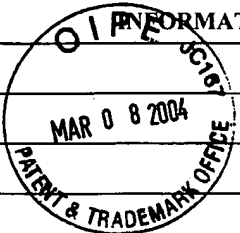
Nicole Teitler Cave 3/3/04  
Nicole Teitler Cave Date

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-30059	
				Application No.: 10/683,636	
				Applicant(s): Nadeem N. Eleyan, et al.	
				Filing Date: October 10, 2003	
				Group Art Unit: 2818	
				Date Submitted: March 3, 2004	
<b>U.S. Patent Documents</b>					
<b>*Examiner Initial</b>		<b>Document Number</b>	<b>Date</b>	<b>Name</b>	
	AA				
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AJ				
	AK				
<b>Foreign Patent Documents</b>					
		<b>Document</b>	<b>Date</b>	<b>Country</b>	<b>Translation</b>
					<b>Yes</b> <b>No</b>
	AL				
	AM				
	AN				
	AP				
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>					
	AR	ELEYAN, ET AL., U.S. Patent Application Publication entitled "Variable Delay Compensation for Data-Dependent Mismatch in Characteristic of Opposing Devices of a Sense Amplifier," Publication No. US 2003/0198112 A1, Published Oct. 23, 2003, pp – 12.			
	AS	ELEYAN, ET AL., U.S. Patent Application No. 10/074,396, entitled "Mechanism to Minimize Failure in Differential Sense Amplifiers," filed Feb. 11, 2002, pp. – 22.			
	AT	ELEYAN, ET AL., U.S. Patent Application No. 10/683,633, entitled "Measuring and Correcting Sense Amplifier and Memory Mismatches Using NBTI," filed Oct. 10, 2003, pp. – 31.			
<b>Examiner</b>		<b>Date Considered</b>			
<p><b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>					